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1	resist\$7 and diode and nano\$9
2	resist\$7 and diode and nano\$9 and @ad<"20031023"
3	resist\$7 and diode and nano\$9 and @ad<"20031023" and foward\$4 near3 bias\$4
4	resist\$7 and diode and nano\$9 and @ad<"20031023" and foward\$4 near3 bias\$4 and revers\$4 near3 bias\$4
5	resist\$7 and diode and nano\$9 and @ad<"20031023" and (alternate ac) with current with source
6	resist\$7 and diode and nano\$9 and @ad<"20031023" and (alternate ac) with current with source and (and-gate or-gate nand-gate)
7	resist\$7 and diode and nano\$9 and @ad<"20031023" and and-gate and or-gate and nand-gate
8	alternate adj current and direct adj current and @ad<"20031023"
9	alternate adj current and direct adj current and @ad<"20031023" and diode and resist\$4
10	alternate adj current and direct adj current and @ad<"20031023" and diode and resist\$4 and ac adj2 (logic\$4 state)
11	"20050091624" and ac with logic\$4 with output
12	"20050091624" and (resist\$4 diode)
13	"20050091624" and superimpos\$5
14	"20050091624" and superimpos\$5 same output
15	nano\$7 and "716"/\$.ccls.
16	nano\$7 with gate and (ac same dc)
17	nano\$7 with gate and (ac same dc) and @ad<"20031023" and impedance
18	nano\$7 with gate and (ac same dc) and @ad<"20031023" and impedance and (ac alternate) with current
19	nano\$7 with gate and ((ac same dc) or (direct same alternate)) same superimpos\$5 and @ad<"20031023" and impedance same (switch\$4 diode)
20	nano\$7 with gate and ((ac same dc) or (direct same alternate)) and @ad<"20031023" and impedance same (switch\$4 diode) and (ac alternate) with current
21	nano\$7 with gate and ((ac same dc) or (direct same alternate)) and @ad<"20031023" and (impedance or (clos\$4 same open\$5)) same (switch\$4 diode) and (ac alternate) with current
22	nano\$7 with gate and (ac alternate) same current and @ad<"20031023"
23	nano\$7 with gate and (ac alternate) same current and @ad<"20031023" and (impedance or (clos\$4 same open\$5)) same (switch\$4 diode)
24	nano\$7 with gate and (ac alternate) same current and @ad<"20031023" and (impedance or (clos\$4 same open\$5)) same (switch\$4 diode) and resist\$5